



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/809,420
Filing Date March 26, 2004
Inventorship Zhongze Wang
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Jennifer M. Kennedy
Attorney's Docket No. MI22-2534
Title Silicon-On-Insulator Comprising Integrated Circuitry and Methods of
Forming Silicon-On-Insulator Circuitry

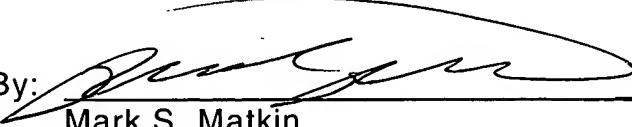
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the patents listed on the attached Form PTO-1449. Pursuant to 1276 OG 55, August 5, 2003, no copies of cited U.S. patents or U.S. patent application publications are included, as the date of filing of this patent application occurs after June 30, 2003. No admission is made regarding whether the submitted reference is prior art.

Respectfully submitted,

Dated: 10-21-04

By: 
Mark S. Matkin
Reg. No. 32,268

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. MI22-2534		SERIAL NO. 10/809,420		
		LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)			APPLICANT: Zhongze Wang				
					FILING DATE March 26, 2004		GROUP 2812		
U.S. PATENT DOCUMENTS									
*Examiner's Initials		Document Number	Date	Name		Class	Subclass	Filing Date If Appropriate	
	AA	6,215,155	04/2001	Wollesen		257	351		
	AB	6,268,630	07/2001	Schwank et al.		257	347		
	AC	6,340,829	01/2002	Hirano et al.		257	347		
	AD	6,552,396	04/2003	Byrant et al.		257	347		
	AE	6,642,579	11/2003	Fung		257	347		
	AF								
	AG								
	AH								
	AI								
FOREIGN PATENT DOCUMENTS									
		Document Number	Date	Country		Class	Subclass	Translation	
	AJ							Yes	No
	AK								
	AL								
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)									
	AM								
	AN								
	AO								
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